|  |  |  |  |
| --- | --- | --- | --- |
| **Feature** | **Working or not** | **Known bug** | **Copied or not** |
| MOV with register addressing mode | Working | None | Not copied |
| MOV with immediate mode | Working | None | Not copied |
| MOV with direct mode | Working | None | Not copied |
| MOV with register indirect mode | Working | None | Not copied |
| ADD with register addressing mode | Working | None | Not copied |
| ADD with immediate mode | Working | None | Not copied |
| ADD with direct mode | Working | None | Not copied |
| ADD with register indirect mode | Working | None | Not copied |
| ADC with register addressing mode | Working | None | Not copied |
| ADC with immediate mode | Working | None | Not copied |
| ADC with direct mode | Working | None | Not copied |
| ADC with register indirect mode | Working | None | Not copied |
| SUB with register addressing mode | Working | None | Not copied |
| SUB with immediate mode | Working | None | Not copied |
| SUB with direct mode | Working | None | Not copied |
| SUB with register indirect mode | Working | None | Not copied |
| SBB with register addressing mode | Working | None | Not copied |
| SBB with immediate mode | Working | None | Not copied |
| SBB with direct mode | Working | None | Not copied |
| SBB with register indirect mode | Working | None | Not copied |
| DIV with register addressing mode | Working | None | Not copied |
| DIV with immediate mode | Working | None | Not copied |
| DIV with direct mode | Working | None | Not copied |
| DIV with register indirect mode | Working | None | Not copied |
| MUL with register addressing mode | Working | None | Not copied |
| MUL with immediate mode | Working | None | Not copied |
| MUL with direct mode | Working | None | Not copied |
| MUL with register indirect mode | Working | None | Not copied |
| XOR with register addressing mode | Working | None | Not copied |
| XOR with immediate mode | Working | None | Not copied |
| XOR with direct mode | Working | None | Not copied |
| XOR with register indirect mode | Working | None | Not copied |
| AND with register addressing mode | Working | None | Not copied |
| AND with immediate mode | Working | None | Not copied |
| AND with direct mode | Working | None | Not copied |
| AND with register indirect mode | Working | None | Not copied |
| OR with register addressing mode | Working | None | Not copied |
| OR with immediate mode | Working | None | Not copied |
| OR with direct mode | Working | None | Not copied |
| OR with register indirect mode | Working | None | Not copied |
| NOP | Working | None | Not copied |
| SHR with register addressing mode | Working | None | Not copied |
| SHR with immediate mode | Working | None | Not copied |
| SHR with direct mode | Working | None | Not copied |
| SHR with register indirect mode | Working | None | Not copied |
| SHL with register addressing mode | Working | None | Not copied |
| SHL with immediate mode | Working | None | Not copied |
| SHL with direct mode | Working | None | Not copied |
| SHL with register indirect mode | Working | None | Not copied |
| SAR with register addressing mode | Working | None | Not copied |
| SAR with immediate mode | Working | None | Not copied |
| SAR with direct mode | Working | None | Not copied |
| SAR with register indirect mode | Working | None | Not copied |
| CLC | Working | None | Not copied |
| SAR with register addressing mode | Working | None | Not copied |
| SAR with immediate mode | Working | None | Not copied |
| SAR with direct mode | Working | None | Not copied |
| SAR with register indirect mode | Working | None | Not copied |
| ROR with register addressing mode | Working | None | Not copied |
| ROR with immediate mode | Working | None | Not copied |
| ROR with direct mode | Working | None | Not copied |
| ROR with register indirect mode | Working | None | Not copied |
| RCL with register addressing mode | Working | None | Not copied |
| RCL with immediate mode | Working | None | Not copied |
| RCL with direct mode | Working | None | Not copied |
| RCL with register indirect mode | Working | None | Not copied |
| RCR with register addressing mode | Working | None | Not copied |
| RCR with immediate mode | Working | None | Not copied |
| RCR with direct mode | Working | None | Not copied |
| RCR with register indirect mode | Working | None | Not copied |
| ROL with register addressing mode | Working | None | Not copied |
| ROL with immediate mode | Working | None | Not copied |
| ROL with direct mode | Working | None | Not copied |
| ROL with register indirect mode | Working | None | Not copied |
| INC with register addressing mode | Working | None | Not copied |
| INC with direct mode | Working | None | Not copied |
| INC with register indirect mode | Working | None | Not copied |
| DEC with register addressing mode | Working | None | Not copied |
| DEC with direct mode | Working | None | Not copied |
| DEC with register indirect mode | Working | None | Not copied |
| Detecting size mismatch | Working | None | Not copied |
| Detecting Memory to memory operation | Working | None | Not copied |
| Detecting Invalid register name | Working | None | Not copied |
| Detecting Incorrect addressing mode | Working | None | Not copied |
| IDIV with register addressing mode | Not Working |  |  |
| IDIV with immediate mode | Not Working |  |  |
| IDIV with direct mode | Not Working |  |  |
| IDIV with register indirect mode | Not Working |  |  |
| IMUL with register addressing mode | Not Working |  |  |
| IMUL with immediate mode | Not Working |  |  |
| IMUL with direct mode | Not Working |  |  |
| IMUL with register indirect mode | Not Working |  |  |
| PUSH with register addressing mode | Not Working |  |  |
| PUSH with immediate mode | Not Working |  |  |
| PUSH with direct mode | Not Working |  |  |
| PUSH with register indirect mode | Not Working |  |  |
| POP with register addressing mode | Not Working |  |  |
| POP with immediate mode | Not Working |  |  |
| POP with direct mode | Not Working |  |  |
| POP with register indirect mode | Not Working |  |  |